

**Society for Applied Microwave Electronics Engineering & Research (SAMEER)**  
**CIT Campus, 2<sup>nd</sup> cross Road, Taramani, Chennai-600 113.**

**Advertisement No. S-CEM/ESTT/PE-PA-PT/01/2026 Dated: 22-02-2026**

**WALK-IN-INTERVIEW FOR CONTRACT POSITION**

Society for Applied Microwave Electronics Engineering & Research, Centre for Electromagnetics (SAMEER-CEM), Chennai, an autonomous scientific institute of the Ministry of Electronics & Information Technology, is a premier Research & Development institution of the Government of India and its headquarters is at Mumbai. Its main objective is to promote the growth of Science and Technology in the areas of Microwave Electronics, Electromagnetics and its allied areas. Other Centers of SAMEER are located at Kolkata, Vishakhapatnam and Guwahati.

SAMEER- CEM, Chennai is looking for young and dynamic professionals for its ongoing projects in contract positions on consolidated salary. The selected candidates will be working in the following areas:

1. **Digital Signal Processing:** There are on-going projects in the Design & Development of Advanced Control system, Communication system and Radar system for various applications. This is an opportunity to work in core areas like embedded systems and FPGA based hardware design, embedded application development in Qt and Keil SW, FPGA based application development, system design and modeling using MATLAB & SIMULINK, Field Deployable System Development etc. Candidates get the opportunity to work with advanced SW and HW development tools. The selected candidates working in this area get opportunity to make strong foundation in various aspects of design and development of Electronic system for Advanced Control applications, communication and radar applications.
2. **Electromagnetic Interference and Compatibility:** This is an opportunity for those who are looking to establishing a career in the domain of EMI/EMC (Electromagnetic Interference and Compatibility), a specialized field currently seeing a huge demand with lot of laboratories and facilities being established across all electronics and electrical industry sectors. The positions offer unique opportunity to work and gain hands-on experience at the world-class EMC Laboratories at the Centre, learning international compliance requirements, evaluation methodologies and troubleshooting complex EMI issues, mentored by scientists working in the area. The experience will make you industry-ready for a domain where global demand is rapidly expanding.
3. **Mechanical And Thermal Engineering:** This is an opportunity for those who are looking to establishing a career in the domain of thermal management of electronics systems. The positions offer unique opportunity to work and gain hands-on experience at the world-class Laboratories, mentored by scientists working in the area. The experience can significantly boost professional value and opens doors to top-tier roles in the global electronics industry.

**The contract tenure will be for two years or till the end of the project, whichever is earlier. These are contractual appointments and shall not confer any right for regularization / absorption or continuation of service in SAMEER.**

## A. Detailed requirements for Candidates for various posts

### 1. Senior Project Engineer (Electronics) and Project Engineer (Electronics)

Approximate vacancies: 20

| <b>a. Senior Project Engineer (Electronics). Post code: SPE01</b> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
|-------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Essential Qualification                                           | 1. B.E / B.Tech. in Electronics and Communication Engineering (ECE) with at least 3 years' experience in the given discipline<br>OR<br>B.E. / B. Tech in Electronics and Communication Engineering (ECE) with ME / M. Tech in relevant discipline<br>2. Minimum of 55% marks or equivalent grade                                                                                                                                                                                                                                                                                                                                                                                              |
| Relevant areas of experience                                      | Experience in any R&D institution or industry in Digital signal processing, Digital systems design using microcontroller & FPGA, Communication system design, Digital signal processing algorithms implementation in MATLAB/SIMULINK, FMCW radar design & development, Application software development using LabVIEW/LabWindows/QT software for electronic systems, Linux OS customization in single board computers etc.                                                                                                                                                                                                                                                                    |
| Selection Procedure                                               | Interview                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Remuneration                                                      | Rs.37,500/- consolidated salary with annual increment of Rs 1,500/-.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Age Limit                                                         | 30 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>b. Project Engineer (Electronics). Post code: PE01</b>         |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| Essential Qualification                                           | B.E / B.Tech. in Electronics and Communication Engineering (ECE)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| Desirable areas of knowledge                                      | Experience in any R&D institution or industry in any of the following areas <ul style="list-style-type: none"> <li>Digital signal processing, Digital systems design using microcontroller &amp; FPGA, Communication system design, Digital signal processing algorithms implementation in MATLAB / SIMULINK, FMCW radar design &amp; development, Application software development using LabVIEW / LabWindows / QT software for electronic systems, Linux OS customization in single board computers etc.</li> <li>Electromagnetic Interference and Compatibility (EMI/EMC) test &amp; evaluation, EMI/EMC calibration, working knowledge of RF and microwave instrumentation etc</li> </ul> |

|                     |                                                                   |
|---------------------|-------------------------------------------------------------------|
| Selection Procedure | Written test followed by Interview                                |
| Remuneration        | Rs.34,000/- consolidated salary with annual increment of Rs 1,360 |
| Age Limit           | 28 years <sup>#</sup>                                             |

## 2. Project Engineer (Mechanical). Post Code: PE02

Approximate vacancies: 2

|                              |                                                                                                                                                              |
|------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Essential Qualification      | B.E / B.Tech. in Mechanical Engineering with Minimum of 55% marks or equivalent grade                                                                        |
| Desirable areas of knowledge | Fresher - preferably with the knowledge in <ul style="list-style-type: none"> <li>Developing 3D models</li> <li>Computational Fluid Dynamics Tool</li> </ul> |
| Selection Procedure          | Written test followed by Interview                                                                                                                           |
| Remuneration                 | Rs.34,000/- consolidated salary with annual increment of Rs 1,360                                                                                            |
| Age Limit                    | 28 years <sup>#</sup>                                                                                                                                        |

## 3. Project Scientific Assistant (Electronics)

Approximate vacancies: 5

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|-----------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>a. Project Scientific Assistant – A (Electronics). Post code: PSAA01</b> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| Essential Qualification                                                     | Diploma in Electronics & Communication Engineering with minimum 55 % marks or equivalent grade                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| Desirable areas of knowledge                                                | Experience in any R&D institution or industry in any of the following areas <ul style="list-style-type: none"> <li>Digital signal processing, Digital systems design using microcontroller &amp; FPGA, Communication system design, Digital signal processing algorithms implementation in MATLAB / SIMULINK, FMCW radar design &amp; development, Application software development using LabVIEW / LabWindows / QT software for electronic systems, Linux OS customization in single board computers etc.</li> <li>Electromagnetic Interference and Compatibility (EMI/EMC) test &amp; evaluation, EMI/EMC calibration, working knowledge of RF and microwave instrumentation etc</li> </ul> |
| Selection Procedure                                                         | Written and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| Remuneration                                                                | Rs.23,500/- consolidated salary with annual increment of Rs 940/-.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| Age Limit                                                                   | 25 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| <b>b. Project Scientific Assistant - B (Electronics). Post code: PSAB01</b> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |

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| Essential Qualification                                                    | Diploma in Electronics & Communication Engineering with minimum 55% marks or equivalent grade with <b>at least 3 years of experience</b>                                                                                                                                                                                                                                                                                         |
| Relevant areas of experience                                               | Experience in any R&D institution or industry in Digital signal processing, Digital systems design using microcontroller & FPGA, Communication system design, Digital signal processing algorithms implementation in MATLAB / SIMULINK, FMCW radar design & development, Application software development using LabVIEW / LabWindows / QT software for electronic systems, Linux OS customization in single board computers etc. |
| Selection Procedure                                                        | Written and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                       |
| Remuneration                                                               | Rs.27,000/- consolidated salary with annual increment of Rs 1080/-.                                                                                                                                                                                                                                                                                                                                                              |
| Age Limit                                                                  | 30 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                                                                                                            |
| <b>c. Project Scientific Assistant –C (Electronics). Post code: PSAC01</b> |                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| Essential Qualification                                                    | Diploma in Electronics & Communication Engineering with minimum 55% marks or equivalent grade with <b>at least 8 years of experience</b>                                                                                                                                                                                                                                                                                         |
| Relevant areas of experience                                               | Experience in any R&D institution or industry in Digital signal processing, Digital systems design using microcontroller & FPGA, Communication system design, Digital signal processing algorithms implementation in MATLAB / SIMULINK, FMCW radar design & development, Application software development using LabVIEW / LabWindows / QT software for electronic systems, Linux OS customization in single board computers etc. |
| Selection Procedure                                                        | Written and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                       |
| Remuneration                                                               | Rs.31,500/- consolidated salary with annual increment of Rs 1,260/-                                                                                                                                                                                                                                                                                                                                                              |
| Age Limit                                                                  | 40 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                                                                                                            |

#### 4. Project Technical Assistant (Electronics)

Approximate vacancies: 1

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|----------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>a. Project Technical Assistant - C (Electronics). Post code: PTAC01</b> |                                                                                                                                                                                                                                                                                                                                           |
| Essential Qualification                                                    | ITI Trade in Electronics with minimum 55% marks or equivalent grade with at least <b>8 years of experience</b>                                                                                                                                                                                                                            |
| Relevant areas of experience                                               | Experience in any R&D institution or industry in any R&D institution or industry in the following areas, assembling of components in PCBs, soldering micro/nano devices, wiring of sub-racks, racks, conformal coating on PCBs, testing of components, PCBs, Analog/ Digital circuits, qualification testing of sub-systems/Systems, etc. |

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| Selection Procedure                                                                                                                                               | Trade and / or Interview                                                                                                                                                                                                                                                                                                                  |
| Remuneration                                                                                                                                                      | Rs.27,000/- consolidated salary with annual increment of Rs 1080/-.                                                                                                                                                                                                                                                                       |
| Age Limit                                                                                                                                                         | 40 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                     |
| <b>b. Project Technical Assistant - B (Electronics). Post code: PTAB01</b><br><i>(to be filled if the post Project Technical Assistant – C is unfilled)</i>       |                                                                                                                                                                                                                                                                                                                                           |
| Essential Qualification                                                                                                                                           | ITI Trade in Electronics with minimum 55% marks or equivalent grade with at <b>least 3 years of experience.</b>                                                                                                                                                                                                                           |
| Relevant areas of experience                                                                                                                                      | Experience in any R&D institution or industry in any R&D institution or industry in the following areas, assembling of components in PCBs, soldering micro/nano devices, wiring of sub-racks, racks, conformal coating on PCBs, testing of components, PCBs, Analog/ Digital circuits, qualification testing of sub-systems/Systems, etc. |
| Selection Procedure                                                                                                                                               | Trade and / or Interview                                                                                                                                                                                                                                                                                                                  |
| Remuneration                                                                                                                                                      | Rs.23,500/- consolidated salary with annual increment of Rs 940/-.                                                                                                                                                                                                                                                                        |
| Age Limit                                                                                                                                                         | 30 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                     |
| <b>c. Project Technical Assistant-A (Electronics). Post code: PTAA01</b><br><i>(to be filled if the post Project Technical Assistant – C &amp; B is unfilled)</i> |                                                                                                                                                                                                                                                                                                                                           |
| Essential Qualification                                                                                                                                           | ITI Trade in Electronics with minimum 55% marks or equivalent grade                                                                                                                                                                                                                                                                       |
| Desirable areas of knowledge                                                                                                                                      | Experience in any R&D institution or industry in any R&D institution or industry in the following areas, assembling of components in PCBs, soldering micro/nano devices, wiring of sub-racks, racks, conformal coating on PCBs, testing of components, PCBs, Analog/ Digital circuits, qualification testing of sub-systems/Systems, etc. |
| Selection Procedure                                                                                                                                               | Trade and / or Interview                                                                                                                                                                                                                                                                                                                  |
| Remuneration                                                                                                                                                      | Rs.21,000/- consolidated salary with annual increment of Rs 840/-                                                                                                                                                                                                                                                                         |
| Age Limit                                                                                                                                                         | 25 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                     |

## 5. Project Technical Assistant (Mechanical)

Approximate vacancies: 1

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|---------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------|
| <b>a. Project Technical Assistant - C (Mechanical). Post code: PTAC02</b> |                                                                                                                                          |
| Essential Qualification                                                   | ITI Trade in Fitter trade with minimum 55% marks or equivalent grade with at <b>least 8 years of experience</b>                          |
| Relevant areas of experience                                              | <ul style="list-style-type: none"> <li>• Relevant mechanical fitting experience in manufacturing of enclosures for electronic</li> </ul> |

|                                                                                                                                                                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
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|                                                                                                                                                                   | <ul style="list-style-type: none"> <li>applications</li> <li>• Basic working knowledge of operation of Lathes, Milling, Drilling, Shaping and Cutting machines.</li> <li>• Working knowledge in operation of precision measuring instruments</li> <li>• Working Knowledge in sheet metal fabrication techniques.</li> <li>• Working Knowledge in prototype model fabrication techniques</li> <li>• Working knowledge in Tapping, helicoil inserting &amp; removing</li> <li>• Maintenance of Stock</li> </ul>                                                                                        |
| Selection Procedure                                                                                                                                               | Trade and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| Remuneration                                                                                                                                                      | Rs.27,000/- consolidated salary with annual increment of Rs 1080/-.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| Age Limit                                                                                                                                                         | 40 years #                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>b. Project Technical Assistant - B (Mechanical). Post code: PTAB02</b><br><i>(to be filled if the post Project Technical Assistant – C is unfilled)</i>        |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| Essential Qualification                                                                                                                                           | ITI Trade in Fitter trade with minimum 55% marks or equivalent grade with at <b>least 3 years of experience.</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Relevant areas of experience                                                                                                                                      | <ul style="list-style-type: none"> <li>• Relevant mechanical fitting experience in manufacturing of enclosures for electronic applications</li> <li>• Basic working knowledge of operation of Lathes, Milling, Drilling, Shaping and Cutting machines.</li> <li>• Working knowledge in operation of precision measuring instruments</li> <li>• Working Knowledge in sheet metal fabrication techniques.</li> <li>• Working Knowledge in prototype model fabrication techniques</li> <li>• Working knowledge in Tapping, helicoil inserting &amp; removing</li> <li>• Maintenance of Stock</li> </ul> |
| Selection Procedure                                                                                                                                               | Trade and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| Remuneration                                                                                                                                                      | Rs.23,500/- consolidated salary with annual increment of Rs 940/-.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| Age Limit                                                                                                                                                         | 30 years #                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| <b>c. Project Technical Assistant –A (Mechanical). Post code: PTAA02</b><br><i>(to be filled if the post Project Technical Assistant – C &amp; B is unfilled)</i> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| Essential Qualification                                                                                                                                           | ITI Trade in Fitter trade with minimum 55% marks or equivalent grade                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| Desirable areas of knowledge                                                                                                                                      | <ul style="list-style-type: none"> <li>• Relevant mechanical fitting experience in</li> </ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |

|                     |  |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
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|                     |  | <p>manufacturing of enclosures for electronic applications</p> <ul style="list-style-type: none"> <li>• Basic working knowledge of operation of Lathes, Milling, Drilling, Shaping and Cutting machines.</li> <li>• Working knowledge in operation of precision measuring instruments</li> <li>• Working Knowledge in sheet metal fabrication techniques.</li> <li>• Working Knowledge in prototype model fabrication techniques</li> <li>• Working knowledge in Tapping, helicoil inserting &amp; removing</li> <li>• Maintenance of Stock</li> </ul> |
| Selection Procedure |  | Trade and / or Interview                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| Remuneration        |  | Rs.21,000/- consolidated salary with annual increment of Rs 840/-                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| Age Limit           |  | 25 years <sup>#</sup>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |

**<sup>#</sup>Age relaxation shall be applicable to SC/ST/OBC/PwD candidates as per Government of India norms**

## **2. General Instructions to Candidates**

| Walk-in Interview Date                                                                                       | <table><tr><th><u>Post</u></th><th><u>Written test-Interview Date</u></th></tr><tr><td><u>SPE01 ( Electronics )</u></td><td><u>09-03-2026</u></td></tr><tr><td><u>PE01 ( Electronics )</u></td><td><u>09-03-2026</u></td></tr><tr><td><u>PE02 ( Mechanical )</u></td><td><u>09-03-2026</u></td></tr><tr><td>PSAA01/PSAA02/PSAC01<br/>(Electronics)</td><td>10-03-2026</td></tr><tr><td>PTAA01/PTAA02/PTAC01<br/>(ITI trade in Electronics &amp; Fitter)</td><td>10-03-2026</td></tr></table> | <u>Post</u>                        | <u>Written test-Interview Date</u> | <u>SPE01 ( Electronics )</u> | <u>09-03-2026</u> | <u>PE01 ( Electronics )</u> | <u>09-03-2026</u> | <u>PE02 ( Mechanical )</u> | <u>09-03-2026</u> | PSAA01/PSAA02/PSAC01<br>(Electronics) | 10-03-2026 | PTAA01/PTAA02/PTAC01<br>(ITI trade in Electronics & Fitter) | 10-03-2026 |
|--------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|------------------------------------|------------------------------|-------------------|-----------------------------|-------------------|----------------------------|-------------------|---------------------------------------|------------|-------------------------------------------------------------|------------|
|                                                                                                              | <u>Post</u>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <u>Written test-Interview Date</u> |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
|                                                                                                              | <u>SPE01 ( Electronics )</u>                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | <u>09-03-2026</u>                  |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
|                                                                                                              | <u>PE01 ( Electronics )</u>                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <u>09-03-2026</u>                  |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
|                                                                                                              | <u>PE02 ( Mechanical )</u>                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | <u>09-03-2026</u>                  |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
|                                                                                                              | PSAA01/PSAA02/PSAC01<br>(Electronics)                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 10-03-2026                         |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
|                                                                                                              | PTAA01/PTAA02/PTAC01<br>(ITI trade in Electronics & Fitter)                                                                                                                                                                                                                                                                                                                                                                                                                                  | 10-03-2026                         |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
| <b>(In case of unavoidable circumstances, interview for shortlisted candidates may continue on next day)</b> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                                    |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
| <b>Reporting Time</b>                                                                                        | <b>9.00- 9.30 AM</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                                    |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
| Test/interview venue                                                                                         | SAMEER-Centre for Electromagnetics, CIT Campus,2 <sup>nd</sup> cross Road, Taramani, Chennai-600 113.                                                                                                                                                                                                                                                                                                                                                                                        |                                    |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |
| Landmark                                                                                                     | SAMEER is located near Government Central Polytechnic (CPT) college and CPT cricket ground within C.I.T. Campus.                                                                                                                                                                                                                                                                                                                                                                             |                                    |                                    |                              |                   |                             |                   |                            |                   |                                       |            |                                                             |            |



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| How to reach? | <p>[1] The SAMEER-CEM campus is approximately 1.5 kms away from Adyar Canal Corner/ Madhya Kailash Temple/ CLRI/Regional Labour Institute Bus Stop and located on the Central Polytechnic Campus.</p> <p>[2] The SAMEER-CEM campus can be reached by the following Bus Route Nos.</p> <ul style="list-style-type: none"> <li>(i). 221/221CT5C: from M.G.R. Central railway station (Get down at WPT/Women's Polytechnic Bus Stop use SAMEER location map to reach by auto or walk)</li> <li>(ii). 23C from Egmore Railway station (Get down at Anna University Bus Stop and use SAMEER location map to reach by auto or walk)</li> <li>(iii). 78: from M.G.R.Koyembedu bus terminus (Get down at Anna University Bus Stop and use SAMEER location map to reach by auto or walk)</li> <li>(iv). 18B: from Broadway (Get down at Anna University Bus Stop and use SAMEER location map to reach by auto or walk)</li> <li>(vi). 19/19A/19B/19BET/19C/M19B from T. Nagar Bus Terminus / Saidapet (Get down at WPT/Women's Polytechnic Bus Stop, use SAMEER location map to reach by auto or walk)</li> </ul> <p>[3] MRTS Train Route: Get down at Indira Nagar MRTS Station in the "Chennai Beach – Velachery" Route.</p> <p>[4] SAMEER location map link - <a href="https://goo.gl/maps/rxuH8">https://goo.gl/maps/rxuH8</a></p> |
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| <p><b>Documents Required to be brought / submitted at the time of Interview</b></p> | <ul style="list-style-type: none"> <li>a) Online Application / Duly filled' application form (available on the SAMEER website)</li> <li>b) Proof of Date of Birth</li> <li>c) Passport size photograph</li> <li>d) SSLC mark Sheet</li> <li>e) HSC mark sheet (as applicable)</li> <li>f) ITI Certificate (for Project Technical Assistant Posts)</li> <li>g) NCVT Certificate (for Project Technical Assistant Posts)</li> <li>h) Qualifying exam passing certificate (as per post applied)</li> <li>i) Qualifying exam marks for all years (as per post applied)</li> <li>j) Experience certificate (duly signed and stamped) as applicable.</li> <li>k) Candidates belonging to SC/ST categories shall be required to submit a valid Caste Certificate issued by the competent authority in the prescribed Government of India format. Candidates belonging to the OBC category shall be required to submit a valid Non-Creamy Layer Certificate. Candidates claiming reservation under the PwD category shall be required to submit a valid Disability Certificate issued by the competent authority, as per Government of India norms. Failure to produce the requisite certificate(s) shall render the candidature liable to rejection.</li> </ul> <p><b>Note: Candidates should bring one set of self attested photocopies of all documents for submission with application along with ORIGINALS for verification.</b></p> |
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### C.GENERAL CONDITIONS

- a. **The above posts carry consolidated pay and appointment will be purely on contract basis.**  
b. **Age limit:** Age limit for all the posts are given below.

| Post name                                      | Post code | Age limit |
|------------------------------------------------|-----------|-----------|
| Project Engineer (Electronics)                 | PE01      | 28        |
| Project Engineer (Mechanical)                  | PE02      | 28        |
| Senior Project Engineer (Electronics)          | SPE01     | 30        |
| Project Scientific Assistant - A (Electronics) | PSAA01    | 25        |
| Project Scientific Assistant - B(Electronics)  | PSAB01    | 30        |
| Project Scientific Assistant - C(Electronics)  | PSAC01    | 40        |
| Project Technical Assistant - A (Electronics)  | PTAA01    | 25        |
| Project Technical Assistant - B (Electronics)  | PTAB01    | 30        |
| Project Technical Assistant - C (Electronics)  | PTAC01    | 40        |
| Project Technical Assistant - A (Mechanical)   | PTAA02    | 25        |
| Project Technical Assistant - B (Mechanical )  | PTAB02    | 30        |
| Project Technical Assistant - C (Mechanical )  | PTAC02    | 40        |

**Age shall be determined based on the date of advertisement. Age relaxation shall be applicable to SC/ST/OBC/PwD candidates as per Government of India norms.**

- c. TA/DA will not be paid to candidate for appearing for screening test and/or interview.
- d. SAMEER reserves the right to reject or accept the candidature of any applicant at any stage without giving any reason.
- e. SAMEER reserves the right to cancel/restrict/enlarge/modify/alter the recruitment process, if need arises, without issuing any further notice or assigning any reason thereof.
- f. Candidate should ensure that they fulfil the eligibility criteria prescribed for the post they have applied. In case it is found out in any stage of the selection process or even after that the candidate has furnished false or incorrect information or suppressed relevant information/material or does not fulfil the criteria, his/her candidature/service are liable for rejection/termination without notice.
- g. Candidate should have completed the qualifying course as on the date of walkin interview. Provisional or original certificate for the qualifying course has to be produced.
- h. Candidate will have to produce all original certificates for verification at the time of interview.
- i. The appointment is purely on contractual basis and shall not confer any right for regularization/absorption or continuation of service in SAMEER.
- j. The person selected for contract appointment will not have any claim or right for his/her continuity in service or automatic extension of the term contract.
- k. Experience beyond the years prescribed in the advertisement shall not be considered for grant of additional increments and no candidate shall have a right to claim such increments.
- l. Experience will be counted post-acquisition of essential qualification.
- m. Only candidates whose M.Tech discipline aligns with the advertisement and/or the desired area of work indicated will be considered as having a relevant M.Tech for the post.
- n. Applications from candidates who have previously been engaged with any SAMEER Centre in the capacity of Research Scientist, Project Engineer (PE) or Research Scientist (RS) for an aggregate period of five (5) years or more shall not be eligible for consideration for appointment to the post of Project Engineer (PE).
- o. Selected candidate will have liability to serve anywhere in the country.

- p. The initial salary of the selected candidate shall be fixed based on experience relevant to the post, with the recommendation of the Selection Committee and with the approval of the competent authority.
- q. Annual increment will be given after each year based on the performance of the selected candidate and on recommendation of the concerned HoD and provided his salary is less than the maximum for his post. The maximum salary for a particular post will be capped at the maximum in for a particular post.
- r. Canvassing in any form and/or bringing in any influence will be treated as disqualification for the post.
- s. No interim queries will be entertained.
- t. Applications of Indian citizens only shall be considered.
- u. Candidates are advised to regularly visit our website for any updates
- v. Candidates desirous of applying for more than one post must apply separately for each post indicating post code no.

**To help in assessing the number of candidates likely to attend the walk in interview we request you to register at the link: <https://forms.gle/K3e2w6bMf88rUoPs7>**

## ANNEXURE

POST CODE:



### **APPLICATION FORM** (TO BE FILLED IN BLOCK LETTERS ONLY)

**Post Applied:** \_\_\_\_\_

**Advt. No.:** \_\_\_\_\_

Affix Recent  
Passport  
Size  
Photograph

- i. Name in full (Shri/Smt/Kum) : \_\_\_\_\_  
-----
- ii. a) Father's name: \_\_\_\_\_ b) Mother's name \_\_\_\_\_
- iii. a) Date of Birth: \_\_\_\_\_ 19\_\_\_\_\_  
(in Christian era in figures)      Date      Month      Year
- b) Age as on closing date mentioned in Advt.: \_\_\_\_\_ Years \_\_\_\_\_ Months \_\_\_\_\_ Days
- iv. a) Sex (Male/Female): \_\_\_\_\_ b) Marital Status : \_\_\_\_\_
- v. Nationality: \_\_\_\_\_
- vi. Reservation Category (SC/ ST): \_\_\_\_\_ 6 (a) Religion: \_\_\_\_\_  
(attach certificates if applicable)
- vii. Whether presently working in Government/Semi-Government/Public Sector  
Undertaking/Autonomous/Corporate Bodies?
- viii. Address for correspondence (with Pin Code)  
\_\_\_\_\_  
(Tele No., Mob. No., Fax & e-mail, if any) \_\_\_\_\_
- ix. Permanent Address (with Pin Code)  
\_\_\_\_\_  
(Tele No., Mob. No., Fax & e-mail, if any) \_\_\_\_\_

x. Educational Qualifications:

| Sl.No | Exam passed Board | Date of joining | Date of Passing | Subject in which degree is awarded | ** Percentage of marks | Division |
|-------|-------------------|-----------------|-----------------|------------------------------------|------------------------|----------|
|-------|-------------------|-----------------|-----------------|------------------------------------|------------------------|----------|

\* In chronological order from X Standard (SSLC/HS/HSC) onwards, # **Specify the gap with reasons in Education career, if any.**

xi. Professional Training:

| Organization | From | Period To | Details of Training |
|--------------|------|-----------|---------------------|
|--------------|------|-----------|---------------------|

xii. Employment Record\* (Attach separate sheet in following format, if necessary):-

| Name & address of employer/ Orgn/Institution | Period of service From | To | Designation of the post held | Scale of pay of each post | Detailed description of work | Reason for leaving |
|----------------------------------------------|------------------------|----|------------------------------|---------------------------|------------------------------|--------------------|
|----------------------------------------------|------------------------|----|------------------------------|---------------------------|------------------------------|--------------------|

\* **Specify the gap, if any**

xiii. Present Basic pay: Total Emoluments: (with break up of basic, DP, DA,HRA,CCA,TA etc)

xiv. Whether the present post is held on regular or ad-hoc basis:

xv. Are you under any contractual obligations to serve Central/State Govt/Any other Public Sector Undertaking or Autonomous body and if so, give details: (attach NOC if applicable)

xvi. Have you been interviewed for any recruitment/selection by SAMEER during the past one year? If yes, give particulars:

| Sr No. | Particulars<br>(eg. Date of Advt, Advt. No.) | Name of Posts &<br>Discipline | Date of<br>Interview | Result |
|--------|----------------------------------------------|-------------------------------|----------------------|--------|
|--------|----------------------------------------------|-------------------------------|----------------------|--------|

xvii. Details of relatives already employed in SAMEER:

| Name of the Relative | Relationship | Lab/Estt in which employed | Post Held |
|----------------------|--------------|----------------------------|-----------|
|----------------------|--------------|----------------------------|-----------|

xviii. Give two referees name & Address and contact Phone number:  
(Not related to the candidates) (Gazetted Officers/Professors  
of reputed academic Institutions/Public Sector Executives etc)

1.

2.

xix. Any other information you may wish to add, including extra-curricular activities (use separate sheet if necessary).

### **Declaration**

I declare that the foregoing information is correct and complete to the best of my knowledge and belief and nothing has been concealed/distorted. If at any time, I am found to have concealed/distorted any material information, my appointment shall be liable to summary termination without notice. If offered appointment, I will join on specified date and subsequently, take up duty in the discharge of SAMEER assignments anywhere in India as and when required.

Place:

Date:

Signature of Candidate

***(Important: use only A4 size paper for application and other testimonials. Attested photocopies of proof of items 3, 6 and 10 should be enclosed)***